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| In re application of: | Lawrence H. Hudepohl Darren M. Jones Radhika Thekkath Franz Treue |
| Serial No.: | 09/751748 |
| Filed: | 12/29/00 |
| Docket: | MIPS.0107-00-US |
| For: | HIGHLY CONFIGURABLE CO-PROCESSOR INTERFACE |

**INFORMATION DISCLOSURE STATEMENT
SUBMITTED UNDER 37 CFR 1.97(D) AND 1.97 (E)(2)**

5 Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the Examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims and return an initialed copy of each form to the undersigned.

10 This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 C.F.R. § 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR § 1.56(b) or is available as a reference under 35 U.S.C. § 102 *et seq.* Applicant reserves the right to swear behind or otherwise disprove the allege "prior" nature of any art cited should the facts support and the situation warrant such an action.

15 It is believed that this disclosure complies with the requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98, and the Manual of Patent Examining Procedures § 609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

20 A copy of each document is enclosed. Some of the documents may have markings thereon. No significance is intended to be attached to the markings.

Respectfully submitted,

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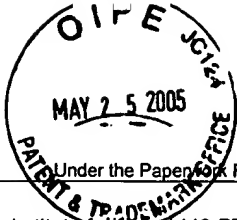
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| Date of Deposit <u>5/25/05</u> . I hereby certify that this paper is being deposited with the U.S. Postal Service Express Mail Post Office to Addressee Service under 37 C.F.R. §1.10 on the date shown above and is addressed to the U.S. Commissioner of Patents and Trademarks, Washington, D.C. 20231. | |
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| Substitute for form 1449-PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i> | | | | Complete if Known | |
| | | | | Application Number | 09/751748 |
| | | | | Filing Date | 12/29/00 |
| | | | | First Named Inventor | Lawrence H. Hudepohl |
| | | | | Art Unit | 2183 |
| | | | | Examiner Name | Allyson Purnell |
| Sheet | 1 | of | 1 | Attorney Docket Number | MIPS.0107-00-US |

| Examiner Initials* | Cite No. ¹ | Document Number | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear |
|--------------------|-----------------------|--|--------------------------------|---|---|
| | | Number-Kind Code ² (if known) | | | |
| | | 6505290 | 01-07-2003 | Moyer et al | |
| | | 5062041 | 10-29-1991 | William S. Zuk | RECEIVED MAY 31 2005 |
| | | 4897779 | 01-30-1990 | Dickson et al | |
| | | 4894768 | 01-16-1990 | Iwasaki et al | |

| NON PATENT LITERATURE DOCUMENTS | | | | OFFICE OF PETITIONS | |
|---------------------------------|-----------------------|---|--|---------------------|----------------|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | | | T ² |
| | AA | JONES, DARREN, "Opal Coprocessor Interface User's Manual," 08/20/99, MIPS Technologies, Inc., Mountain View, CA, US | | | |
| | AB | BRUB, ROLF-JURGEN. <i>RISC, The MIPS-R3000 Family</i> . Berlin and Munich: Siemens Aktiengesellschaft., 1991. pp51-65 | | | |
| | AC | RIORDAN, TOM. <i>MIPS R3010 Floating-point Coprocessor Interface</i> . MIPS Computer Systems, Inc. October 27, 1988. | | | |
| | AD | Motorola, Inc. "MC68020 and MC68EC020 Microprocessors User's Manual, 1st Edition," 1992, pp.7-10 to 7-14 | | | |
| | AE | <i>LR3010 and LR3010A, MIPS Floating-Point Accelerator User's Manual</i> . LSI Logic Corporation. 1991. | | | |
| | AF | <i>LR3000 and LR3000A, MIPS RISC Microprocessor User's Manual</i> . LSI Logic Corporation. 1991. pp.i to 1-16 and 9-1 to 9-10. | | | |
| | AG | <i>R3000A Processor Interface</i> . Preliminary. March 9, 1990. | | | |

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|--------------------|--|-----------------|--|
| Examiner Signature | | Date Considered | |
|--------------------|--|-----------------|--|

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 27 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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